Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | LEE ET AL. | Examiner | Art Unit | Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Weste et al., Principles of CMOS VLSI Design: A Systems Perspective, Addison-Wesley Publishing Company, 1993, second edition, section 10.5.4, pages 408-411			
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